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## \*BIBDATASHEET\*

CONFIRMATION NO. 3437

Bib Data Sheet

SERIAL NUMBER	FILING OR 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
09/843,687	04/30/2001	716	2825	43889-937
RULE				

## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 08/803,145 02/19/1997 PAT 6,282,506

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 8-032152 02/20/1996

## F REQUIRED, FOREIGN FILING LICENSE GRANTED \*\*

05/15/2001

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	26	11	10
Verified and Acknowledged	Examiner's Signature _____ Initials _____				

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## TITLE

METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT UTILIZING A SCAN TEST FUNCTION

FILING FEE RECEIVED 1570	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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